

Fig. 1

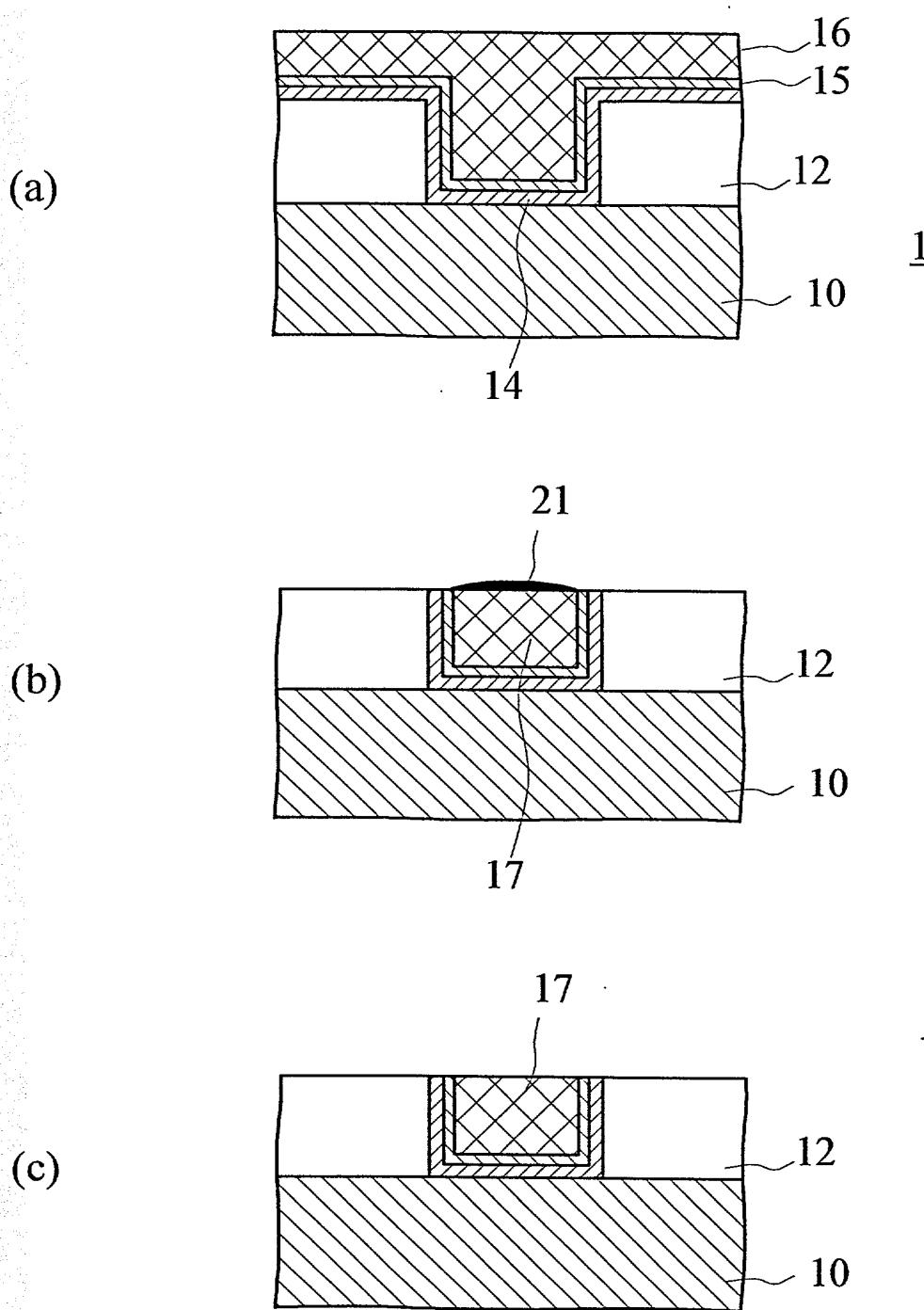


Fig. 2

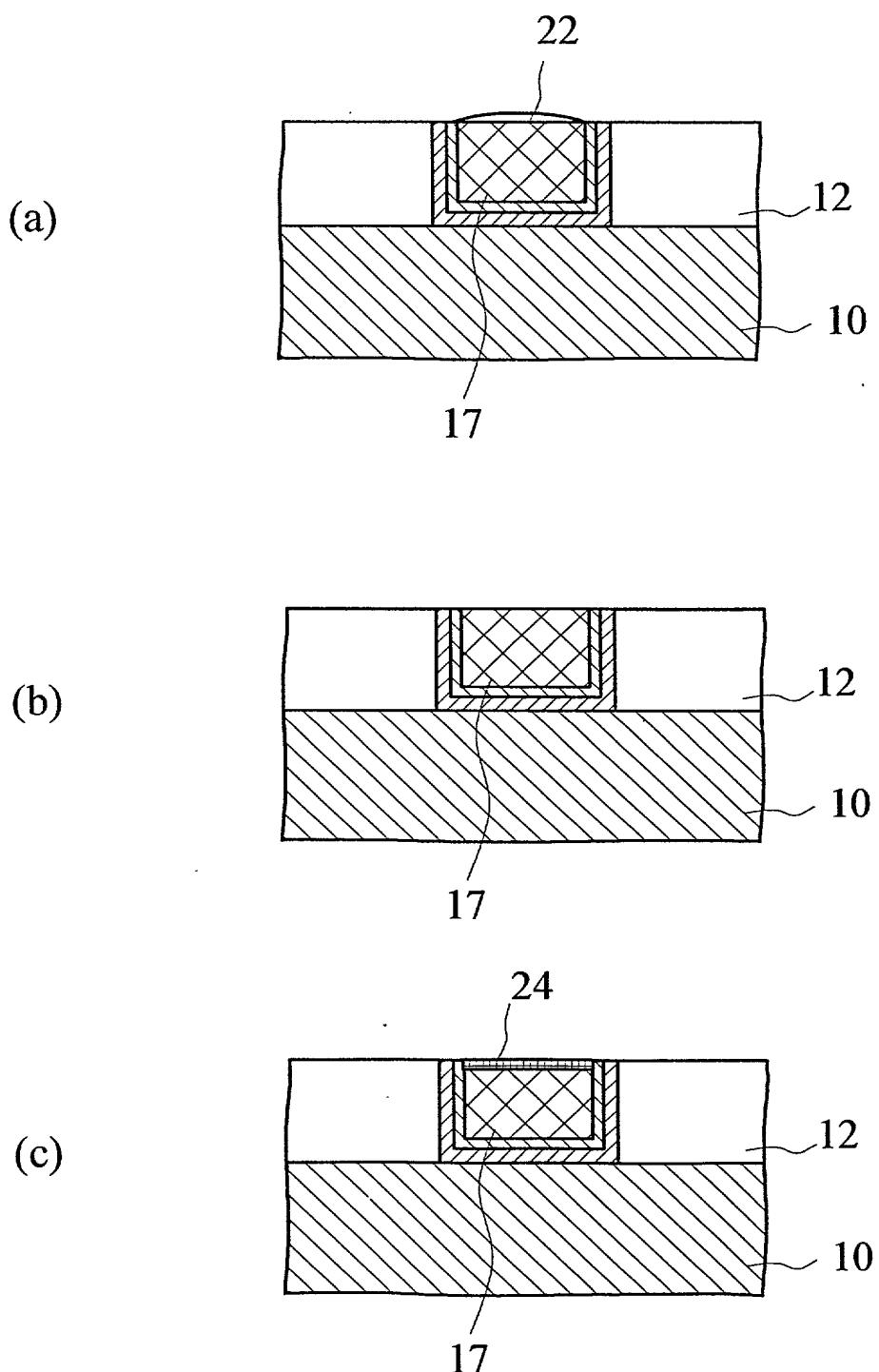


Fig. 3

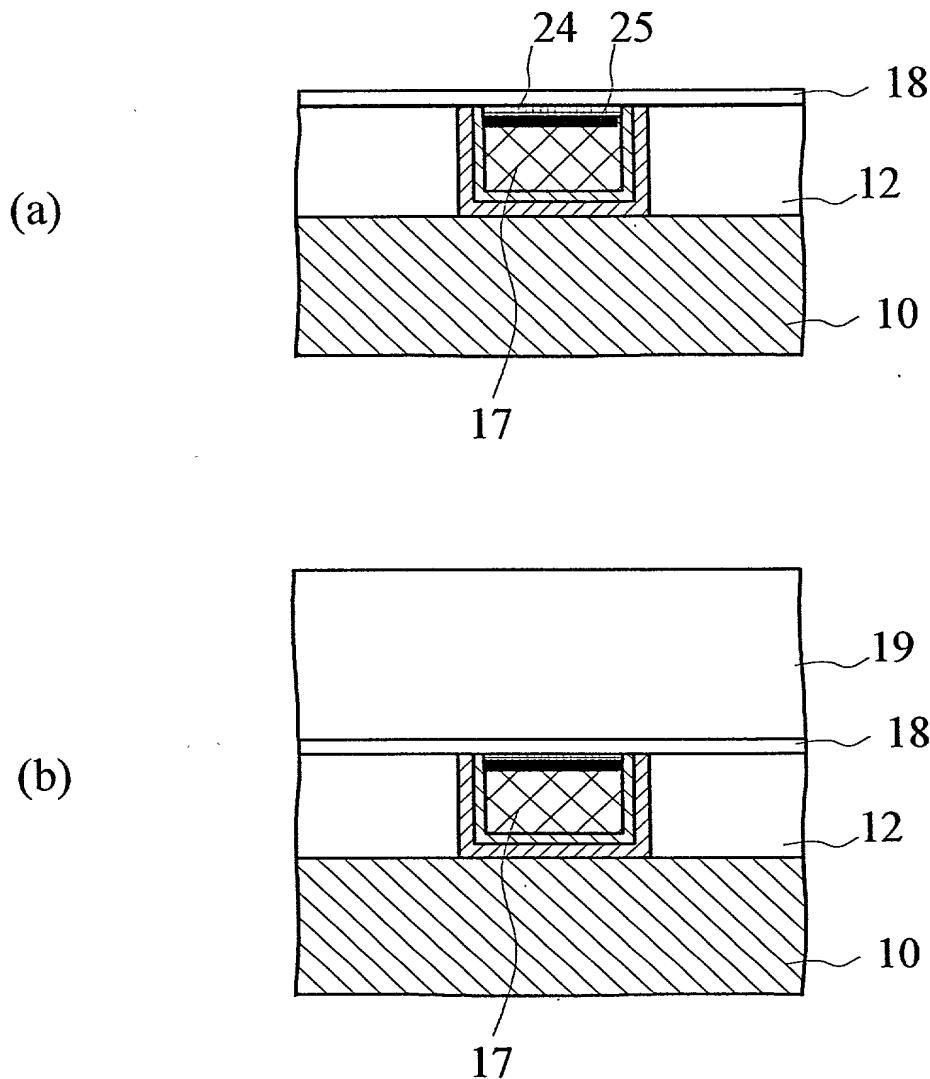
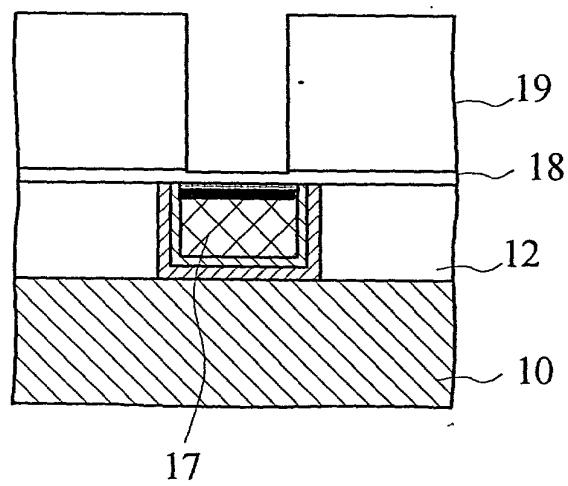
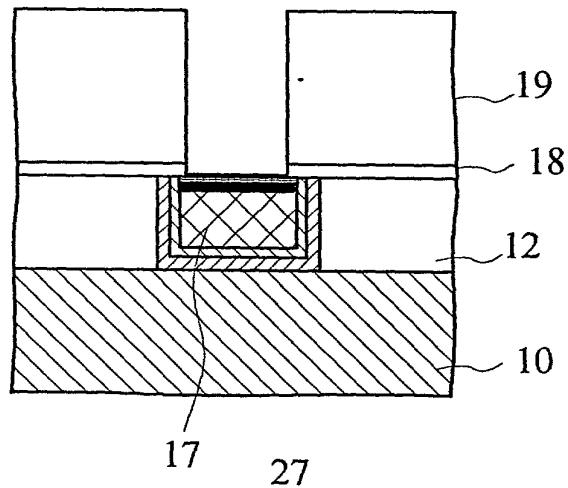


Fig. 4

(a)



(b)



(c)

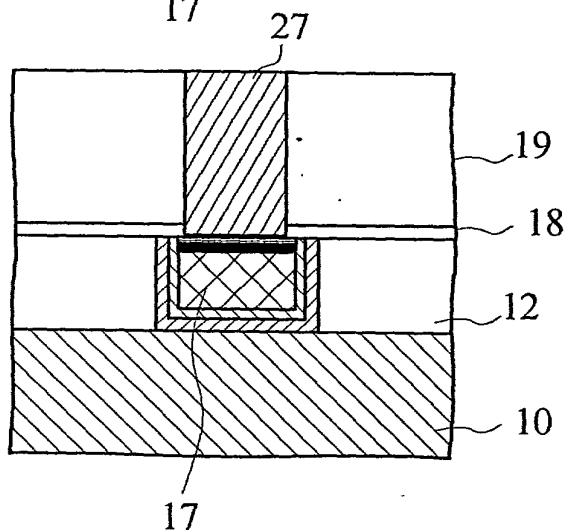


Fig. 5

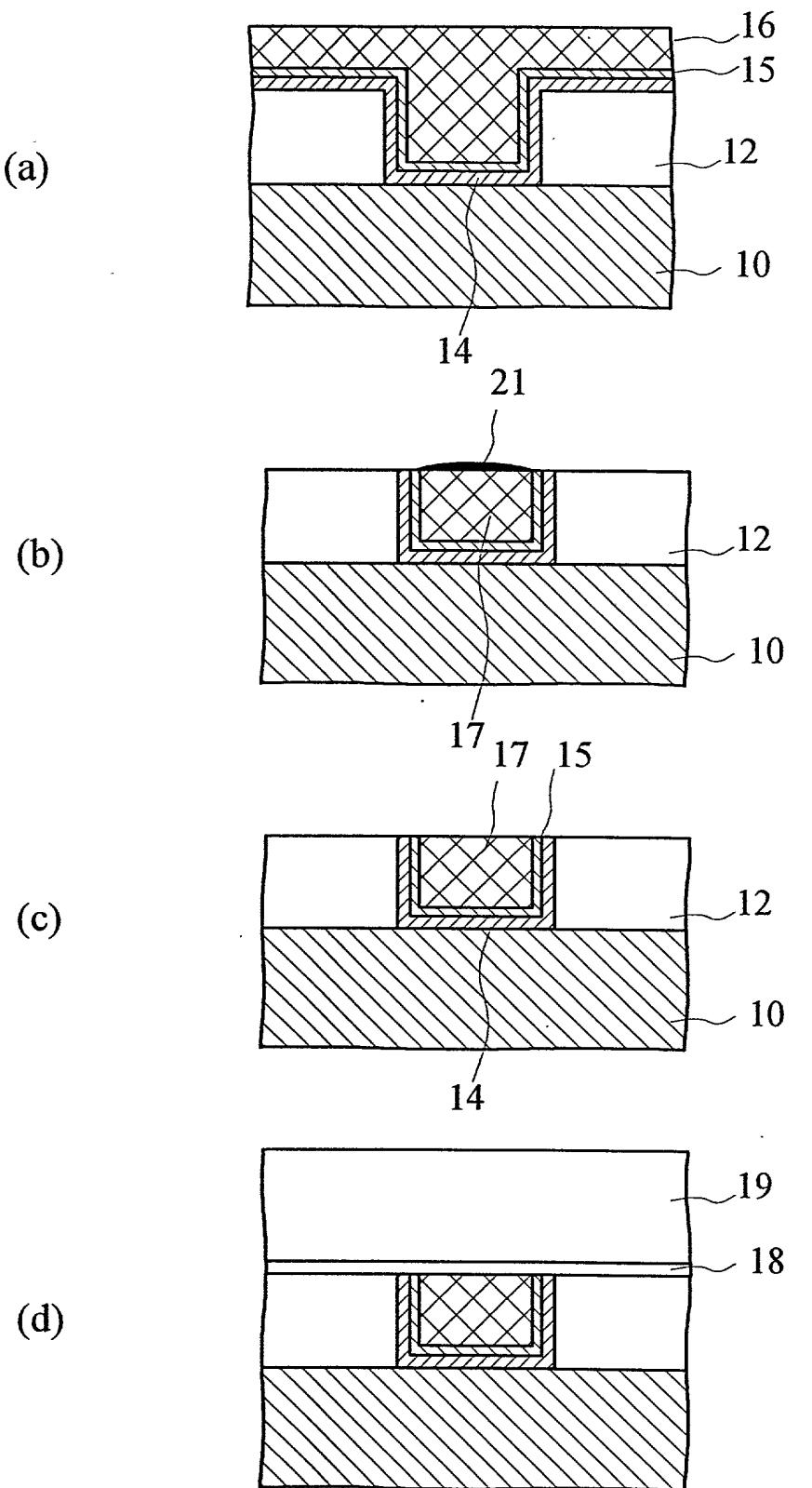
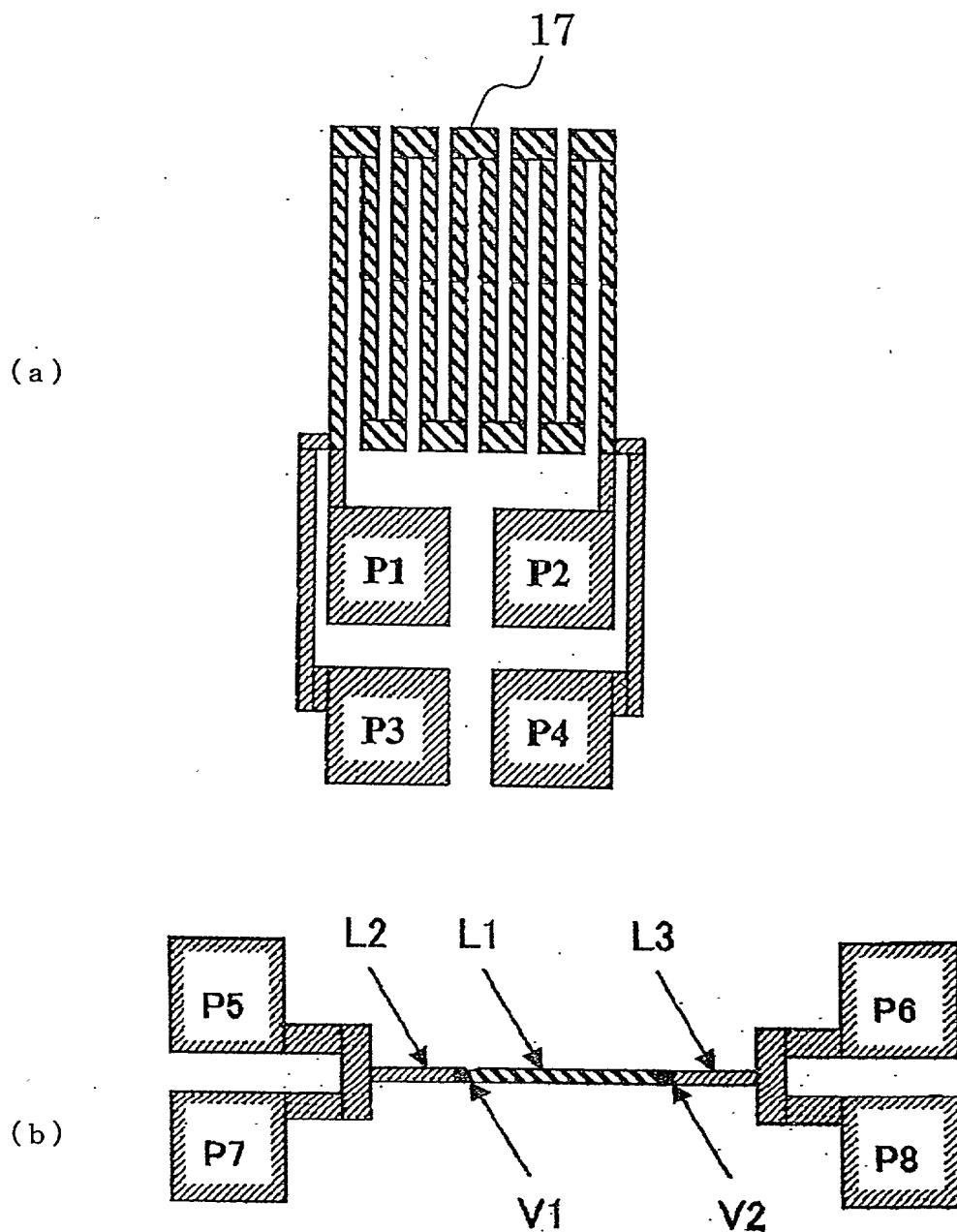


Fig. 6



L 1 : Measured Section of the Interconnection

L 2 , L 3 : Interconnections lying at the
Upper Level Than L1

V 1 : Via Plug to Connect L1 with L2

V 2 : Via Plug to Connect L1 with L3

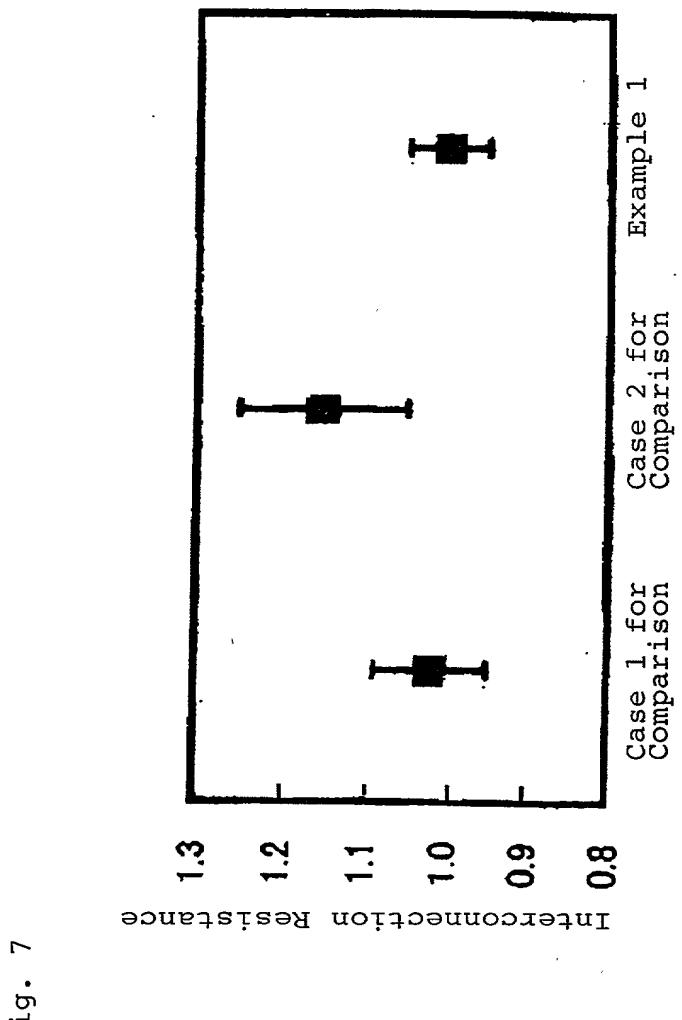


Fig. 7

Fig. 8

